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Abstract: A powerful reference for anyone
working in power electronics. The topic,
switched-mode power converters, has
gathered immense interest for the power
electronics community worldwide. This is
mostly due to the number of emerging
applications that benefit from the

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availability of low-cost digital technology,
and therefore becomes competitive against
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controllers for high precision power converters with ultra-high effective switching frequency. This involves research in nonlinear (switched and multi-rate) control structures, stabilizing, predictive and self-learning control theory, and real-time controller implementation.

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It is important that these high-frequency disturbances do not alias down and appear in the bandwidth of the control system, and so anti-aliasing filters are also generally required in such systems. In contrast to mechanical servo systems, it is thus generally necessary in active noise and vibration systems to include both anti-

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aliasing and reconstruction filters in the design, and the specification of these filters is discussed in the following two sections.

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and design of digital control loops around high-frequency switched-mode power converters in a systematic and rigorous manner. Comprehensive treatment of digital control theory for power converters. Verilog and VHDL sample codes are provided.

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This thesis develops digital pulse-width-modulation (DPWM) control of switching power converters. A target application is microprocessor voltage regulation which requires high efficiency and tight output load-line control.

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Nevertheless, practical digital control of high-frequency switched-mode power converters has moved from proof-of-concept demonstrations to digital pulse width modulation (DPWM) controller

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chips commercially available from multiple vendors, with growing adoption rates in many applications. Several factors have

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