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Discover a fresh approach to efficient and insight-driven analog integrated circuit design in nanoscale-CMOS with this hands-on guide. Expert authors present a sizing methodology that employs ...

~~Systematic Design of Analog CMOS Circuits~~

Finishing up on the topic of CMOS bus logic I am going to show a ... Switching gears I am going to talk a bit about the physical layout of integrated circuits. Shown is an old wafer I have ...

~~How CMOS Works: Some Final Words About CMOS~~

With CMOS silicon fabs, you can scale up production very rapidly," Bowers added. The shift toward integrated photonics is not only evident at traditional semiconductor giants such as Intel, but also ...

~~Integrated Silicon Photonics Driving High-Speed Optical Interconnects~~

SkyWater Technology, Inc (NASDAQ: SKYT) bagged $15 million in funding from the Department of Defense (DOD) to facilitate open source design for its 90 nm process offering, part of the previously ...

~~Google, SkyWater Expand Collaboration Over Open Source Chip Design~~

Challenging the prevailing opinion that circuit simulation can reveal all problems in CMOS circuits, Masakazu Shoji maintains that simulation cannot completely remove the often costly errors that ...

~~Theory of CMOS Digital Circuits and Circuit Failures~~

Mixed-signal design flow enables RF CMOS chip By Rami Ahola, EEdesign February 28, 2003 (3:56 p.m. EST) URL: http://www.eetimes.com/story/OEG20021212S0064 Spirea AB ...

~~Mixed-signal design flow enables RF CMOS chip~~

If you're not familiar with the 555 timer, suffice it to say that this versatile integrated ... bipolar design had difficulty generating output pulses down to 50% duty cycle, the CMOS variants ...

~~Making The World's Fastest 555 Timer, Or Using A Modern IC Version~~

SkyWater Technology (NASDAQ: SKYT), the trusted technology realization partner, today announced it is receiving $15 million in funding from the Department of Defense (DOD) to facilitate open source ...

~~SkyWater Receives Funding from DOD, Partners with Google to Facilitate Open Source Design for its new 90 nm Technology Offering~~

The development of nanoscale CMOS technology and the consequent design of nanoscale integrated circuits underpins the continuous rapid development in ICT. Independent journalism costs money.

~~Student award in microelectronics~~

SiTune Corporation is a multi-disciplinary and strong engineering team which has demonstrated great achievement and execution in the areas of RF, ASIC, system design and software to produce the most ...

~~SiTune unveils Innovative mobile DVB-T CMOS demodulator exceeding MBRAI specification.~~

By Chris Wack SkyWater Technology Inc. said it is receiving $15 million in funding from the Defense Department to help open source design for ...

~~SkyWater Technology Gets $15 Mln in Funding From U.S. Defense Department~~

Panelists at recent 6G symposium discuss performance constraints of current chips and infrastructure. The long-anticipated industry transition from 4G to 5G posed many challenges for hardware and ...

~~6G Transition Will Challenge Comm Hardware~~

For more information about Keysight's partnership with Synopsys, see "Synopsys Collaborates with Keysight Technologies to Deliver Integrated Custom Design Flow for 5G Designs." *RF CMOS technology is ...

~~Keysight, Synopsys Collaborate in TSMC's N6RF Design Reference Flow~~

The One RS 1-Inch 360 Edition features a pair of 1-inch CMOS ... for integrated stabilization without the need for a standalone gimbal. The camera also features an interchangeable lens design ...