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Design of a Low Noise
Amplifier in 0.18 μ m

SiGe BiCMOS ... In

recent years, the design

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of 60GHz low noise amplifier mainly has two kinds circuit structure, one is the single ended structure, the other is the differential

structure60GHz low noise amplifier needed to □ Design of a 60 GHz Low Noise Amplifier in a 013 m SiGe ...

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A low power 60-GHz on-off-keying (OOK) receiver is implemented in a commercial 90nm RF CMOS process. The receiver includes on-chip antenna, LNA, 60GHz detector and limiting amplifier, all ...

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~~front end and ...~~

60 GHz receiver design is to correctly predict the expected maximum input signal (usually originating from interference) and adjust the required linearity accordingly, as it is directly

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60 GHz Receiver ...~~

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The design of a 60 GHz low loss hybrid phase shifter with 360 degree phase shift Abstract:

This paper presents a 60 GHz low loss phase shifter characterized by 360 degree phase shift and low variation of insertion loss using GaAs pHEMT process.

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M. Kraemer 2010
Worldwide regulations
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allow the

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A low noise ampli er is
designed for future
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60GHz band, using an existing SiGe technology, BiCMOS8HP from IBM. Different topologies are analyzed and compared. To different schematics of single ended three stage designs are compared. A differential four stage CE topology is designed and simulated with parasitic extraction.

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~~Design of a 60GHz Low-
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Low-power 60 GHz low-noise amplifier (LNA) with a 9.379 dB peak gain and a 4.500 dB minimum NF is demonstrated in a GaAs Based technology. The LNA is composed of three stage of cascaded common ...

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Abstract: This paper presents the design of a low-power millimeter wave receiver for Gbps short range wireless communications in the 60GHz frequency range. The scope of this paper covers the system design of the OOK direct conversion

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receiver, the design of a novel 60GHz low-noise amplifier, the co-design of the mixer with the IF amplifier and the design of a IF variable gain amplifier.

~~Design of a low power
60GHz OOK receiver in
65nm CMOS ...~~

The most common
wireless applications in
1994 were still

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broadcast TV and cellular systems. Both of these operated in the much lower range of 600MHz to roughly 2GHz bands. Design and operation of 60GHz mmwave systems was very difficult and expensive. The ecosystem of components, test gear and more just did not exist.

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This architecture has been used in the production of a low-cost, 60-GHz module that supported a data rate greater than 155 Mbps. Variations of the complete chip-set architecture can be used to...

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Design and

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SPNT RF-MEMS

Switches M. W.

Rousstia and M. H. A. J.

Herben Eindhoven

University of

Technology,

Department of Electrical

Engineering,

Electromagnetics

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Group, P.O. Box 513,
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The Netherlands, +31
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□ A design of a
capacitive-shunt RF-
MEMS

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analysis of low loss
60 GHz SPNT ...~~

Design of 60-GHz low-
noise amplifiers with
low NF and robust ESD

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Design in 65-nm
CMOS January 2013
IEEE Transactions on
Microwave Theory and
Techniques
61(1):553-561

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low NF and ...~~

considering this one.
Merely said, the Design
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60GHz, the unilateral gain is 8.6dB while the MSG is 6.3dB. For 60GHz circuit design, the reactive component values are very small, requiring inductance values on the order of $50 \sim 150$ pH. Direct implementation of these elements using spiral

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inductors do not have the required accuracy, so transmission lines are used with the

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Design and implementation of a 60GHz digitally-controlled passive phase shifter. It consists of a

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differential transmission
loaded with a
differential MOS
varactor at each side. It
achieves low cost,
simple design, low
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full 2.3Gbps capacity of the radio is available divided into both directions for transmit and receive under user control.

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